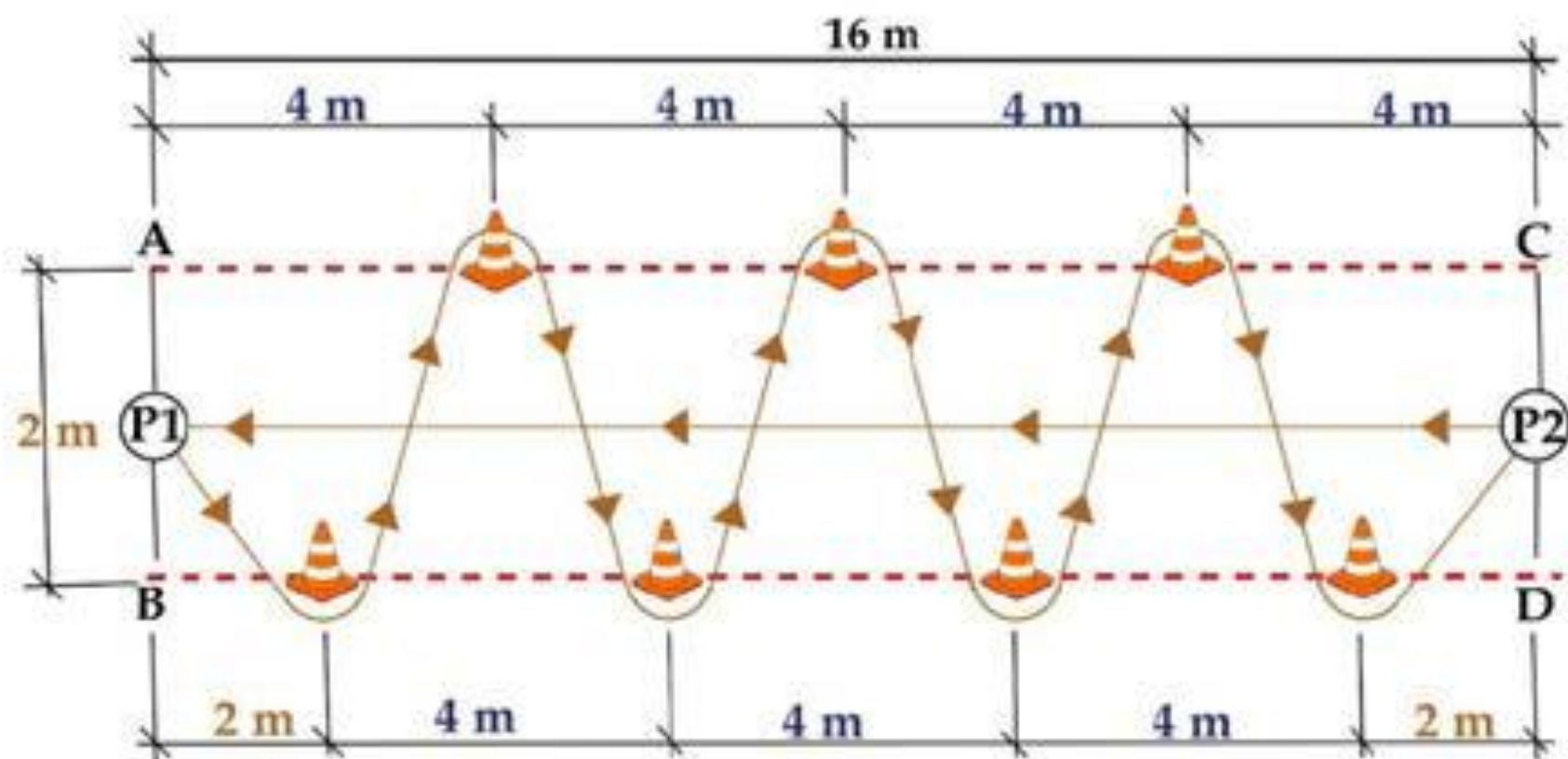




*Esquema del circuito de agilidad-velocidad*